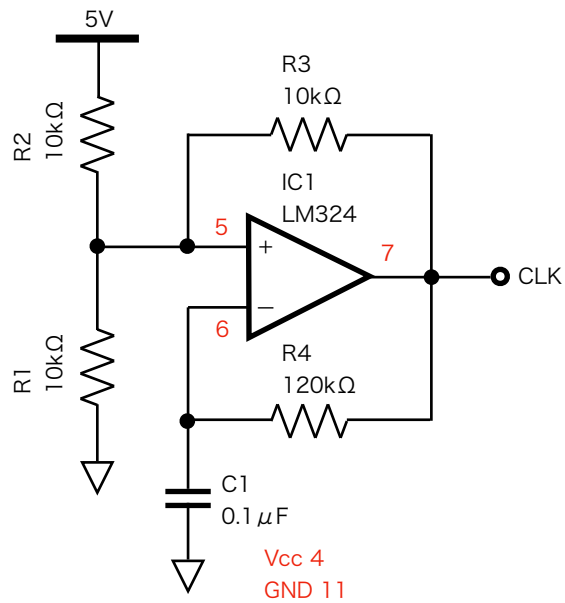
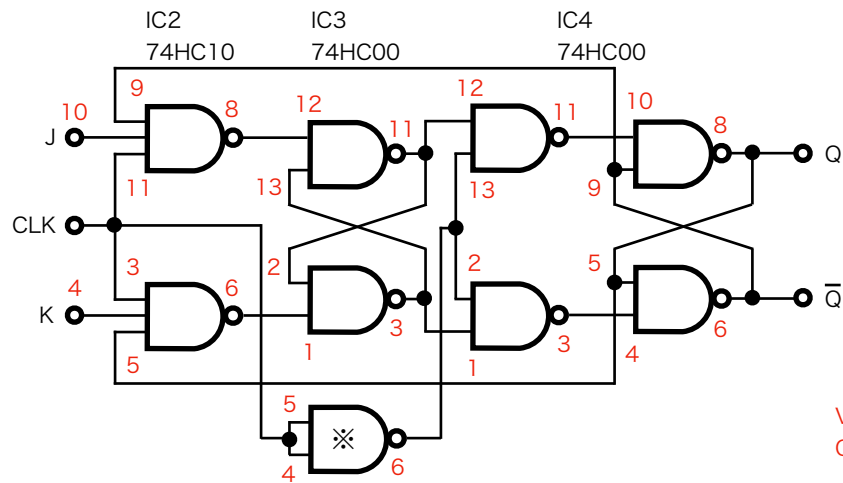


Master-Slave JK Flip-flop

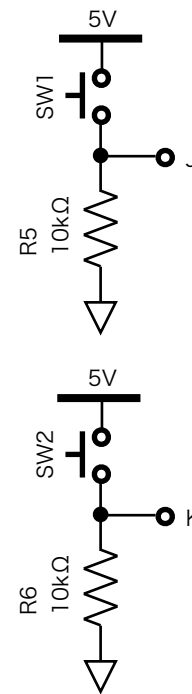


クロック発生回路

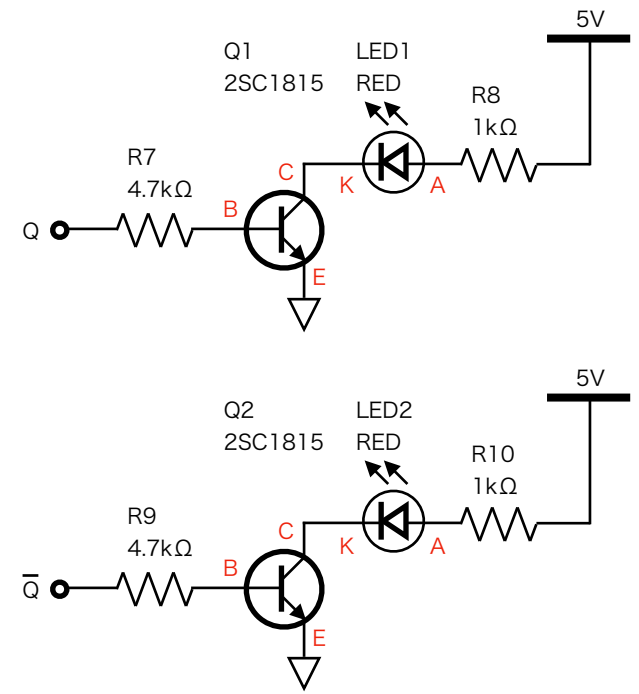


※ NOTをNANDで代用

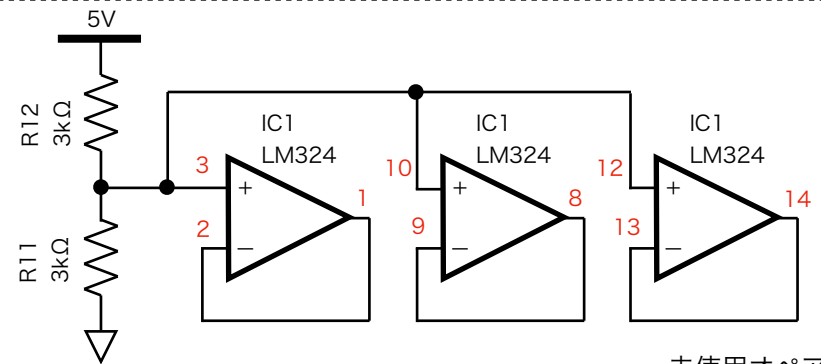
Master-Slave JK Flip-flop



入力回路



出力回路



未使用オペアンプ回路